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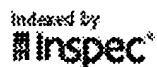
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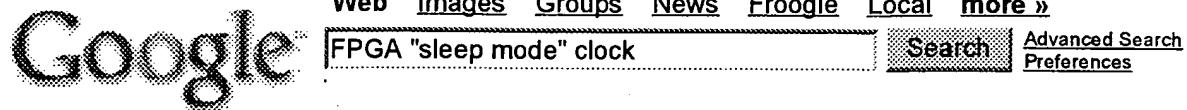
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Due to the extensive number of SRAM cells within SRAM **FPGA** interconnects, ...

Likewise, keeping a microprocessor in its **sleep mode** also lengthens battery ...

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An integrated Watchdog Timer with it's own **clock** signal provides protection ...

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So most instructions are executed within 1 system **clock** period, ... If the device

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